



TIME TABLE FOR 5TH SEMESTER(S) W.E.F 1/7/2024

EE & EE(IC) ENGINEERING

TIME → DAYS ↓	9.30 AM TO 10.25 AM	10.25 AM TO 11.20 AM	11.20 AM TO 12.15 PM	12.15 PM TO 1.10 PM	1.10 PM TO 1.45 PM	1.45 PM TO 4.30 PM
M O N	DE&M (Y.M)	EC-II(B.K.M) WPBC(A.R.B)	PE&PLC (S.K)	EM&ST (D.D.B)	EM&ST (D.D.B)	EM-II LAB (B.K.M/N.N)
						A&DC LAB (S.K)
T U E	A&DC (S.K)	EM&ST (D.D.B)	DE&M (Y.M)	EC-II(B.K.M) WPBC(A.R.B)	PE&PLC LAB (S.K)	PE&PLC LAB (S.K)
	UEET A.K.B					
W E D	DE&M (Y.M)	EC-II(B.K.M) WPBC(K.M)	PE&PLC (S.K)	EM&ST (D.D.B)	EM&ST (D.D.B)	SCA (A.R.B)
T H U	EM&ST (D.D.B)	A&DC (S.K)	DE&M (Y.M)	EC-II(B.K.M) WPBC(A.R.B)	DE&M LAB (Y.M)	DE&M LAB (Y.M)
F R I	EC-II(B.K.M)	PE&PLC (S.K)	EM&ST (D.D.B)	A&DC (S.K)	A&DC (S.K)	PROJECT PHASE - 1 (Y.M/D.D.B)
	WPBC(A.R.B)					
S A T	PE&PLC S.K	EM&ST (D.D.B)	A&DC (S.K)	UEET (A.R.B)	DE&M (Y.M)	EM-II LAB (B.K.M/N.N)

**R
E
C
E
S
S**

J. K. K.
28/06/24
HOD

ELECTRICAL ENGG

[Signature]
Admission-In-Charge
SSIT (Polytechnic), Kara iya, MBJ.

[Signature]
28/06/24
PRINCIPAL
SSIT, KALAKAD, MBJ